Application No.	Applicant(s)	
09/228 562	TANIGUCHI ET AI	
Examiner	Art Unit	
Walter F Briney III	2644	
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	D9/228,562 Examiner Walter F Briney III Depars on the cover sheet with IS (OR REMAINS) CLOSED in IS) or other appropriate communication is suitally 2004. The communication is suitally 2004. The communication is an incommunication to file incommunication. The communication is an incommunication to file incommunication. The communication is application. The	D9/228,562 Examiner Walter F Briney III Deers on the cover sheet with the correspondence address-IS (OR REMAINS) CLOSED in this application. If not included 55) or other appropriate communication will be mailed in due course. THRIGHTS. This application is subject to withdrawal from issue at the initial and MPEP 1308. DIVY 2004. Deer Teceived. Deer Teceived in Application No Decreased been received in Application No Decreased been received. Decreased been received in Application No Decreased been received. Decreased

Allowable Subject Matter

Claims 1-21 are allowed.

As allowable subject matter has been indicated, applicant's reply must either comply with all formal requirements or specifically traverse each requirement not complied with. See 37 CFR 1.111(b) and MPEP § 707.07(a).

In particular, replacement drawing sheets must be submitted, in order to comply with the Notice of Draftsperson's Patent Drawing Review on 05 June 2002.

The following is a statement of reasons for the indication of allowable subject matter:

It is the opinion of this examiner that the newly recited limitations of claims 1, 11, and 21 have placed all claims of the application in condition for allowance.

In particular, the applicant has reported that in a telephonic interview with the previous examiner of record, held 21 November 2003, it was determined that Kobayashi does not fairly teach or disclose the limitation, wherein "at least one of the first LC filter circuit unit and the second LC filter circuit unit includes two resonant portions connected via a capacitor." See page 13 of the RCE. This is clearly true. Attention is drawn to figure 5, wherein an input (A and A') to a circuit includes two dual parallel-resonant filter comprising C8, C9, C10, C11, L6, L7, L8, L9, and L10. One of the dual parallel-resonant circuits comprises C8, C9, L6, and L7. The parallel-resonant circuit of C8 and L6 is directly connected to parallel-resonant circuit C9 and L7. Therefore, Kobayashi does not teach the limitation recited above.

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This examiner further submits that neither Kato nor Lopez makes up for this deficiency. While figure 11 of Lopez depicts a filter including dual-parallel-mode circuits and a connecting capacitor, there is no suggestion or motivation to use said combining capacitor in the invention of Kobayashi. In particular, the orientation of the resonant circuits in Kobayashi and Lopez are different, and adding coupling capacitors could have unexpected or detrimental results.

In further review of the invention of Lopez, it appears that the filter disclosed therein has a strong resemblance to that of the current invention. However, the main difference is made apparent by the limitation, "wherein said common line side of said first LC filter circuit unit is electrically and directly connected to said common side line of said second LC filter circuit unit via said common line." The first and second filters of figure 11 are not connected directly to each other. They share a common connection to a ground terminal, however, this suggests that they indirectly connected, as ground terminals are often spread out. In addition, even if they were considered to be directly connected they share no common electronic connection to each other, as signals passing through one ground terminal do not reappear at another. This appears to be different than the invention, wherein a common phase point is defined, which serves to cancel fluctuations in a differential input.

A detailed search of US class 333 -- Wave Transmission Lines and Networks, subclasses 167-178 -- was performed in addition to the above analysis, however, no new art was uncovered.

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Therefore, claim 1 is allowable over the prior art of record, and claims 11, and 21, which include the limitations discussed above are also allowable. In addition, claims 2-10 and 12-20 are allowable because of their dependence on claims 1 and 11.

Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Walter F Briney III whose telephone number is 703-305-0347. The examiner can normally be reached on M-F 8am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Forester W Isen can be reached on 703-305-4386. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

WFB 10/27/04

PRIMARY EXAMINER

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